



Reg. No. :

Name :

**Seventh Semester B.Tech. Degree Examination, April 2015
(2008 Scheme)**

**08.705 (2) : COMPUTER HARDWARE AND INTERFACING
(Elective – II) (R)**

Time : 3 Hours

Max. Marks : 100

PART – A

Answer **all** questions. **Each** question has **4** marks.



1. Why is SRAM not preferred as primary system memory ?
2. What is SO-DIMM ? Explain.
3. Find the number of data and address lines required to address 1TB of memory with a 32-bit processor.
4. Describe the settings included under the control of CMOS set up.
5. What are the various types of SCSI termination ?
6. Explain zoned bit recording.
7. Explain screen resolution and color depth in the context of video adapters.
8. Compare serial and parallel ports.
9. Why does DVD store more data than CD ?
10. What is AGP ?



PART – B

Answer **any one full** question from **each** Module. **Each full** question has **20** marks.

Module – I

11. a) Explain the various chipsets available on the mother board. 10
b) What are the functions of chipset ? 10
12. a) Describe the various DRAM memory types. 10
b) Explain front side bus, backside bus and level 1 and level 2 caches. 10

Module – II

13. a) Briefly explain power supply form factors. 12
b) Describe the various processor cooling methods. 8
14. a) Describe the mechanisms of reading and writing data in magnetic storage media. 10
b) Discuss the various ATA standards. 10

Module – III

15. a) Compare AGP and PCI system bus. 8
b) Briefly describe the architecture of MCA bus. 12
16. a) Write notes on MIDI. 10
b) Explain the various video adapter standards. 10